

AMD64 boot->kernel handoff

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What it affects

- locore
- create_pagetable
- EFIRT
- KEXEC
- KVG2B

How amd64 kernel starts

- kernel .text+.data are put at physical 2M (KERNLOAD)
- mode is set to Long (64bit %cs)
- 4-level page table maps low 1G 1:1, then same 1G is repeated till end of VA (*KERNLOAD* → *KERNBASE*)
- jump to btext in locore
- locore asm sets up initial bootstack and calls hammer_time()
- hammer_time() -> pmap_bootstrap() -> create_pagetables()

- System memory map can be incompatible with kernel put at KERNLOAD phys
- e.g. EFIRT might claim that memory
- KEXEC wants to put kernel at the place of existing kernel (copying trampoline)
- KASLR ...

- Kernel must be made relocatable
- ... but still use kernel memory model and mapped at upper 2G
- Something must relocate it (loader or kernel ?)
- Anyway this changes handoff interface
- Flag day