

























































































































































XED_IFORM_VUNPCKLPS_YMMf32_MASKmskw_YMMf32_MEMf32_AVX512	vunpcklps ymm1{k1}, ymm2, [rdi]	1	1
XED_IFORM_VUNPCKLPS_YMMf32_MASKmskw_YMMf32_YMMf32_AVX512	vunpcklps ymm1{k1}, ymm2, ymm3	1	1
XED_IFORM_VUNPCKLPS_ZMMf32_MASKmskw_ZMMf32_MEMf32_AVX512	vunpcklps zmm1{k1}, zmm2, [rdi]	8	1
XED_IFORM_VUNPCKLPS_ZMMf32_MASKmskw_ZMMf32_ZMMf32_AVX512	vunpcklps zmm1{k1}, zmm2, zmm3	1	1
XED_IFORM_XORPD_XMMpd_MEMpd	vxorpd xmm1, xmm1, [rdi]	7	0.5
XED_IFORM_XORPD_XMMpd_XMMpd	vxorpd xmm1, xmm1, xmm2	1	0.33
XED_IFORM_VXORPD_XMMdq_XMMdq_MEMdq	vxorpd xmm1, xmm2, [rdi]	7	0.5
XED_IFORM_VXORPD_XMMdq_XMMdq_XMMdq	vxorpd xmm1, xmm2, xmm3	1	0.33
XED_IFORM_VXORPD_XMMf64_MASKmskw_XMMf64_MEMf64_AVX512	vxorpd xmm1{k1}, xmm2, [rdi]	7	0.5
XED_IFORM_VXORPD_XMMf64_MASKmskw_XMMf64_XMMf64_AVX512	vxorpd xmm1{k1}, xmm2, xmm3	1	0.33
XED_IFORM_VXORPD_YMMqq_YMMqq_MEMqq	vxorpd ymm1, ymm2, [rdi]	1	0.5
XED_IFORM_VXORPD_YMMqq_YMMqq_YMMqq	vxorpd ymm1, ymm2, ymm3	1	0.33
XED_IFORM_VXORPD_YMMf64_MASKmskw_YMMf64_MEMf64_AVX512	vxorpd ymm1{k1}, ymm2, [rdi]	1	0.5
XED_IFORM_VXORPD_YMMf64_MASKmskw_YMMf64_YMMf64_AVX512	vxorpd ymm1{k1}, ymm2, ymm3	1	0.33
XED_IFORM_VXORPD_ZMMf64_MASKmskw_ZMMf64_MEMf64_AVX512	vxorpd zmm1{k1}, zmm2, [rdi]	8	0.5
XED_IFORM_VXORPD_ZMMf64_MASKmskw_ZMMf64_ZMMf64_AVX512	vxorpd zmm1{k1}, zmm2, zmm3	1	0.5
XED_IFORM_XORPS_XMMps_MEMps	vxorps xmm1, xmm1, [rdi]	7	0.5
XED_IFORM_XORPS_XMMps_XMMps	vxorps xmm1, xmm1, xmm2	1	0.33
XED_IFORM_VXORPS_XMMdq_XMMdq_MEMdq	vxorps xmm1, xmm2, [rdi]	7	0.5
XED_IFORM_VXORPS_XMMdq_XMMdq_XMMdq	vxorps xmm1, xmm2, xmm3	1	0.33
XED_IFORM_VXORPS_XMMf32_MASKmskw_XMMf32_MEMf32_AVX512	vxorps xmm1{k1}, xmm2, [rdi]	7	0.5
XED_IFORM_VXORPS_XMMf32_MASKmskw_XMMf32_XMMf32_AVX512	vxorps xmm1{k1}, xmm2, xmm3	1	0.33
XED_IFORM_VXORPS_YMMqq_YMMqq_MEMqq	vxorps ymm1, ymm2, [rdi]	1	0.5
XED_IFORM_VXORPS_YMMqq_YMMqq_YMMqq	vxorps ymm1, ymm2, ymm3	1	0.33
XED_IFORM_VXORPS_YMMf32_MASKmskw_YMMf32_MEMf32_AVX512	vxorps ymm1{k1}, ymm2, [rdi]	1	0.5
XED_IFORM_VXORPS_YMMf32_MASKmskw_YMMf32_YMMf32_AVX512	vxorps ymm1{k1}, ymm2, ymm3	1	0.33
XED_IFORM_VXORPS_ZMMf32_MASKmskw_ZMMf32_MEMf32_AVX512	vxorps zmm1{k1}, zmm2, [rdi]	8	0.5
XED_IFORM_VXORPS_ZMMf32_MASKmskw_ZMMf32_ZMMf32_AVX512	vxorps zmm1{k1}, zmm2, zmm3	1	0.5
XED_IFORM_VZEROALL	vzeroall	16	4
XED_IFORM_VZERoupper	vzeroupper	4	1
XED_IFORM_XADD_MEMb_GPR8	xadd [rdi], al	7	1
XED_IFORM_XADD_MEMv_GPRv	xadd [rdi], rax	7	1
XED_IFORM_XADD_GPR8_GPR8	xadd al, cl	3	0.75
XED_IFORM_XADD_GPRv_GPRv	xadd rax, rcx	3	0.75
XED_IFORM_XCHG_MEMb_GPR8	xchg [rdi], al	10	1.25
XED_IFORM_XCHG_MEMv_GPRv	xchg [rdi], rax	10	1.25
XED_IFORM_XCHG_GPR8_GPR8	xchg cl, al	3	0.75
XED_IFORM_XCHG_GPRv_GPRv	xchg rcx, rax	1	0.25
XED_IFORM_XGETBV	xgetbv	2	0.5
XED_IFORM_XOR_MEMb_IMMb_80r6	xor [rdi], 1	6	1
XED_IFORM_XOR_MEMv_IMMz	xor [rdi], 1	6	1
XED_IFORM_XOR_MEMv_IMMb	xor [rdi], 1	6	1
XED_IFORM_XOR_MEMb_IMMb_82r6	xor [rdi], 1	6	1
XED_IFORM_XOR_MEMb_GPR8	xor [rdi], al	6	1
XED_IFORM_XOR_MEMv_GPRv	xor [rdi], rax	6	1
XED_IFORM_XOR_GPR8_MEMb	xor al, [rdi]	6	0.5
XED_IFORM_XOR_GPR8_IMMb_80r6	xor al, 1	1	0.25
XED_IFORM_XOR_GPR8_IMMb_82r6	xor al, 1	1	0.25
XED_IFORM_XOR_AL_IMMb	xor al, 1	1	0.25
XED_IFORM_XOR_GPR8_GPR8_30	xor al, cl	0	0.25
XED_IFORM_XOR_GPR8_GPR8_32	xor al, cl	0	0.25
XED_IFORM_XOR_OrAX_IMMz	xor ax, 1	1	0.25
XED_IFORM_XOR_GPRv_MEMv	xor rax, [rdi]	6	0.5
XED_IFORM_XOR_GPRv_IMMz	xor rax, 1	1	0.25
XED_IFORM_XOR_GPRv_IMMb	xor rax, 1	1	0.25
XED_IFORM_XOR_GPRv_GPRv_31	xor rax, rcx	0	0.25
XED_IFORM_XOR_GPRv_GPRv_33	xor rax, rcx	0	0.25
XED_IFORM_XRSTOR_MEMmxsave	xrstor ptr [rdi]	37	8
XED_IFORM_XRSTOR64_MEMmxsave	xrstor64 ptr [rdi]	37	8
XED_IFORM_XSAVE_MEMmxsave	xsave ptr [rdi]	42	11
XED_IFORM_XSAVE64_MEMmxsave	xsave64 ptr [rdi]	41	10
XED_IFORM_XSAVEOPT_MEMmxsave	xsaveopt ptr [rdi]	46	11
XED_IFORM_XSAVEOPT64_MEMmxsave	xsaveopt64 ptr [rdi]	46	11
XED_IFORM_XSETBV	xsetbv	5	1.25