

IA-32 Intel[®] Architecture and Intel[®] Extended Memory 64 Technology Software Developer's Manual

Documentation Changes

August 2004

Notice: The IA-32 Intel[®] Architecture and Intel[®] Extended Memory 64 Technology may contain design defects or errors known as errata that may cause the product to deviate from published specifications. Current characterized errata are documented in the specification updates.

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Revision History	. 4
Preface	. 5
Summary Table of Changes	. 6
Documentation Changes	. 7



Revision History

Version	Description	Date
-001	Initial Release	November 2002
-002	Added 1-10 Documentation Changes. Removed old Documentation Changes items that already have been incorporated in the published Software Developer's manual	December 2002
-003	 Added 9 -17 Documentation Changes Removed Documentation Change #6 - References to bits Gen and Len Deleted Removed Documentation Change #4 - VIF Information Added to CLI Discussion 	February 2003
-004	Removed Documentation changes 1-17 Added Documentation changes 1-24	June 2003
-005	Removed Documentation Changes 1-24Added Documentation Changes 1-15	September 2003
-006	Added Documentation Changes 16- 34	November 2003
-007	 Updated Documentation changes 14, 16, 17, and 28. Added Documentation Changes 35-45. 	January 2004
-008	Removed Documentation Changes 1-45 Added Documentation Changes 1-5	March 2004
-009	Added Documentation Changes 7-27	May 2004
-010	Removed Documentation Changes 1–27 Added Documentation Changes 1	August 2004



This document is an update to the specifications contained in the Affected Documents/Related Documents table below. This document is a compilation of documentation changes. It is intended for hardware system manufacturers and software developers of applications, operating systems, or tools.

Affected Documents/Related Documents

Document Title	Document Number
IA-32 Intel [®] Architecture Software Developer's Manual: Volume 1, Basic Architecture	253665
IA-32 Intel [®] Architecture Software Developer's Manual: Volume 2A, Instruction Set Reference	253666
IA-32 Intel [®] Architecture Software Developer's Manual: Volume 2B, Instruction Set Reference	253667
IA-32 Intel [®] Architecture Software Developer's Manual: Volume 3, System Programming Guide	253668
Intel® Extended Memory 64 Technology Software Developer's Guide Volumes 1 and 2	300835

Nomenclature

Documentation Changes include errors or omissions from the current published specifications. These changes will be incorporated in the next release of the Software Development Manual.



Summary Table of Changes

The following table indicates documentation changes which apply to the IA-32 Intel Architecture. This table uses the following notations:

Codes Used in Summary Table

Change bar to left of table row indicates this erratum is either new or modified from the previous version of the document.

Summary Table of Documentation Changes

Number	Documentation Changes
1.	The mechanism for handling the Extended Family ID and Extended Model ID fields has been updated



Documentation Changes

1. The mechanism for handling the Extended Family ID and Extended Model ID fields has been updated

In Chapter 3, *IA-32 Intel Architecture Software Developer's Manual, Volume 2B*; the CPUID section has been updated. Text defining the recommended mechanisms for handling the Extended Family ID and the Extended model ID fields have been updated. The updated text is reproduced below.

The Extended Family ID is to be examined only if the Family ID is 0FH; the Extended Model ID is to be examined only if the Family ID is 0FH or 06H. Often software displays processor information as a combination of family, model and stepping.

The recommended mechanism for integrating Family ID fields into a display follows:

Displayed_Family = (Extended_Family_ID) (8-bits) + Family_ID (4-bits zero extended to 8-bits)

The recommended mechanism for integrating Model ID fields into a display follows:

Displayed_Model = ((Extended_Model_ID (4-bits) << 4))(8-bits) + Model_ID (4-bits zero extended to 8-bits)

Documentation Changes

